

<<Correct Hardware Des>>

图书基本信息

书名：<<Correct Hardware Design and Verification Methods 正确的硬件设计与验证法>>

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作者：Geist, Daniel; Tronci, Enrico; Geist, Daniel

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内容概要

This book constitutes the refereed proceedings of the 12th IFIP WG 10.5 Advanced Research Working Conference on Correct Hardware Design and Verification Methods, CHARME 2003, held in L'Aquila, Italy in October 2003. The 24 revised full papers and 8 short papers presented were carefully reviewed and selected from 65 submissions. The papers are organized in topical sections on software verification, automata based methods, processor verification, specification methods, theorem proving, bounded model checking, and model checking and applications.

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书籍目录

Invited Talks What Is beyond the RTL Horizon for Microprocessor and System Design? The Charm of Abstract Entities Tutorial The PSL/Sugar Specification Language A Language for all Seasons Software Verification Finding Regularity: Describing and Analysing Circuits That Are Not Quite Regular Predicate Abstraction with Minimum Predicates Efficient Symbolic Model Checking of Software Using Partial Disjunctive Partitioning Processor Verification Instantiating Uninterpreted Functional Units and Memory System: Functional Verification of the VAMP A Hazards-Based Correctness Statement for Pipelined Circuits Analyzing the Intel Itanium Memory Ordering Rules Using Logic Programming and SAT Automata Based Methods On Complementing Nondeterministic Buchi Automata Coverage Metrics for Formal Verification Efficient LTL Model Checking Short Papers 1 An Optimized Symbolic Bounded Model Checking Engine Constrained Symbolic Simulation with Mathematica and ACL2 Semi-formal Verification of Memory Systems by Symbolic Simulation CTL May Be Ambiguous When Model Checking Moore Machines Specification Methods Reasoning about GSTE Assertion Graphs Towards Diagrammability and Efficiency in Event Sequence Languages Executing the Formal Semantics of the Accellera Property Specification Language by Mechanised Theorem Proving Protocol Verification On Combining Symmetry Reduction and Symbolic Representation for Efficient Model Checking..... Short Paers 2 Theorem Proving Bounded Model Checking Model Checking and Application Author Index

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