

<<宽带高动态范围DAC>>

图书基本信息

书名 : <<宽带高动态范围DAC>>

13位ISBN编号 : 9787030182449

10位ISBN编号 : 7030182448

出版时间 : 2007-1

出版时间 : 科学出版社

作者 : 桃瑞丝

页数 : 204

版权说明 : 本站所提供之下载的PDF图书仅提供预览和简介,请支持正版图书。

更多资源请访问 : <http://www.tushu007.com>

<<宽带高动态范围DAC>>

内容概要

宽带高动态范围DAC是现代信息系统的基本构件。
目前的电流舵DAC具有在较宽的频率范围内取得高动态性能的潜力。
然而，它们在更高频率时的性能被非线性限制了。
宽带高动态范围DAC是解决这一缺陷的有效方法。
《宽带高动态范围DAC（影印版）》的亮点在于提出了详细的方案，可以解决由失配和时钟串扰引起的时序误差，实现了12位、采样率500M、0.18微米工艺的高性能DAC。

<<宽带高动态范围DAC>>

书籍目录

PrefaceGlossaryAbbreviations1 Digital to Analog conversion concepts1.1 Functional aspects1.1.1 Definition of the D/A function1.1.2 Functional specifications1.2 Algorithmic aspects1.3 Signal processing aspects1.3.1 Waveforms and Line coding1.3.2 Signal Modulation concepts1.4 Circuit aspects1.4.1 Architecture terminology1.4.2 Resistive voltage division architectures1.4.3 Capacitive voltage and charge division architectures1.4.4 Current division based architectures1.5 Conclusions2 Framework for Analysis and Synthesis of DACs2.1 Overview2.2 Framework description2.2.1 Analysis2.2.2 Synthesis3 Current Steering DACs3.1 Basic circuit3.1.1 Partitioning and segmentation3.1.2 Current switching network and current sources3.1.3 Clock-data synchronization circuit3.1.4 Auxiliary circuits3.2 Implementations and technology impact4 Dynamic limitations of Current Steering DACs4.1 State of the art in dynamic linearity4.2 Dynamic limitations of current steering DACs4.2.1 Matching and relative amplitude precision4.2.2 Matching and relative timing precision4.3 Conclusions5 Current Steering DAC circuit error analysis5.1 Amplitude domain errors5.1.1 Relative amplitude inaccuracies5.1.2 Output resistance modulation5.2 Time domain errors5.2.1 Nonlinear settling and output impedance modulation5.2.2 Asymmetrical switching5.2.3 Modulation of switching behavior5.2.4 Charge feedthrough and injection5.2.5 Relative timing inaccuracies5.2.6 Power supply bounce and substrate noise5.2.7 Clock (timing) jitter5.3 Conclusions6 High-level modeling of Current Steering OACs6.1 System modeling6.1.1 System layers6.1.2 System excitations and responses6.1.3 System parameters6.1.4 Subsystem interaction6.1.5 System modulation6.2 Error properties and classification6.2.1 Error properties6.2.2 Error classification6.3 Functional error generation mechanisms6.3.1 Definitions6.3.2 Algorithmic modeling6.3.3 Functional modeling6.3.4 Examples.....7 Functional modeling of timing errors8 Functional analysis of local timing errors9 Circuit analysis of local timing errors10 Synthesis concepts for CS DACs11 Design of a 12 bit 500 Msample/s DACReferencesA Output spectrum for timing errorsB Literature data

<<宽带高动态范围DAC>>

版权说明

本站所提供下载的PDF图书仅提供预览和简介，请支持正版图书。

更多资源请访问:<http://www.tushu007.com>