

<<计算机系统>>

图书基本信息

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前言

There is excitement when you talk to high school students about computers . There is a sense of mystery as to what is “ inside the box ” that makes the computer do such things as play video games with cool graphics . play music —_be it rap or symphony——send instant messages to friends , and so on . The purpose behind this textbook is to take the journey together to discover the mystery of what is inside the box As a glimpse of what is to come , let us say at the outset that what makes the box interesting is not just the hardware , but also how the hardware and the system software work in tandem to make it all happen Therefore . the path we take in this book is to look at hardware and software together to see how one helps the other and how together they make the box interesting and useful We call this approach “ unraveling the box ” —that is . resolving the mystery of what is inside the box : We look inside the box and understand how to design the key hardware elements (processor , memory, and peripheral controllers) and the OS abstractions needed to manage all the hardware resources inside a computer, including processor , memory, I / O and disk , multiple processors , and network . Hence , this is a textbook for a first course in computer systems embodying a novel integrated approach to these topics .

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内容概要

本书采用集成方法，系统地讲解了计算机系统的软件和硬件知识。全书分为5个模块：处理器、内存系统、存储系统、并行系统和网络，分别介绍并讨论了处理器及其相关的软件问题、内存系统和内存分级体系、I/O和文件系统、操作系统问题及支持并行编程的多处理器中相应体系结构特点、网络硬件的发展和处理各种网络行为的网络协议堆的特点等。通过本书，读者将洞悉计算机软件和硬件之间的相互作用，加深对现代操作系统的理解。

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作者简介

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Umakishore Ramachandran，1986年获得威斯康星大学麦迪逊分校计算机专业博士学位.现在是佐治亚理工学院计算机系教授，STAR Center&Korean Programs中心主任.其主要研究兴趣是体系结构设计、程序设计和并行分布式系统分析。
他曾获得NSF授予的美国总统青年科学家奖、佐治亚理工学院优秀博士论文指导奖等。
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书籍目录

Preface
Chapter 1 Introduction
Chapter 2 Processor Architecture
Chapter 3 Processor Implementation
Chapter 4 Interrupts, Traps, and Exceptions
Chapter 5 Processor Performance and Pipelined Processor Design
Chapter 6 Processor Scheduling
Chapter 7 Memory Management Techniques
Chapter 8 Details of Page-Based Memory Management
Chapter 9 Memory Hierarchy
Chapter 10 Input/Output and Stable Storage
Chapter 11 File System
Chapter 12 Multithreaded Programmin and Multiprocessors
Chapter 13 Fundamentals of Networking and Network Protocols
Chapter 14 Epilogue: A Look Back at the Journey

章节摘录

插图：Applications have in the past influenced, and continue to date to influence, the design of instruction set. In the 1970s, and perhaps into the 1980s, computers were used primarily for number crunching in scientific and engineering applications. Such applications rely heavily on floating-point arithmetic. Whereas high-end computers (such as IBM 370 series and Cray) included such instructions in their ISA, the so-called minicomputers of that era (such as DEC PDP 11 series) did not. There were successful companies (e.g., Floating Point Systems, Inc.) that made attached processors for accelerating floating-point arithmetic for the minicomputers. Nowadays, floating-point instructions are a part of any general-purpose processor. Processors (e.g., StrongARM, ARM) that are used in embedded applications such as cell phones and PDAs may not have such instructions. Instead, they realize the effect of floating-point arithmetic by integer instructions for supporting math libraries. Another example of applications' influence on the ISA is the MMX instructions from Intel. Applications that process audio, video, and graphics deal with streaming data—that is, continuous data such as a movie or music. Such data would be represented as arrays in the memory. The MMX instructions, first introduced by Intel in 1997 in their Pentium line of processors, aimed at dealing with streaming data efficiently by the CPU. The intuition behind these instructions is pretty straightforward. As the name stream data suggests, audio, video, and graphics applications require the same operation (such as addition) to be applied to corresponding elements of two or more streams. Therefore, it makes sense to have instructions that mimic this behavior. The MMX instructions originally introduced in the Pentium line and its successors do precisely that. There are 57 instructions, grouped into categories such as arithmetic, logical, comparison, conversion, shift, and data transfer, and each one takes two operands (each of which is not a scalar, but a vector of elements).

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《计算机系统·集成方法(英文版)》特色：采用启发式教学方法，以问题驱动，激发读者的学习兴趣；逐步探索，揭开计算机系统的神秘面纱。
以丰富的实例阐明相关概念及问题，加深读者对所学知识的理解。
提供了丰富的历史背景知识。
并就某些问题探讨了其未来发展趋势,便于读者融会贯通。
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